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Term:

lighting logic

Display:

10

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-

Starting with Number

1

Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

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Logout

Interrupt

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<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
<u>L1</u>	lighting logic	33	<u>L1</u>

END OF SEARCH HISTORY

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

Terms	Documents
L1 and logic unit	26

Database:

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L2

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<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L2</u>	L1 and logic unit	26	<u>L2</u>
<u>L1</u>	lighting logic	33	<u>L1</u>

END OF SEARCH HISTORY

WEST[Generate Collection](#)[Print](#)**Search Results - Record(s) 1 through 10 of 26 returned.**☐ 1. Document ID: US 20030112246 A1

L2: Entry 1 of 26

File: PGPB

Jun 19, 2003

PGPUB-DOCUMENT-NUMBER: 20030112246

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030112246 A1

TITLE: Blending system and method in an integrated computer graphics pipeline

PUBLICATION-DATE: June 19, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Kirk, David B.	San Francisco	CA	US	
Sabella, Paolo E.	Pleasanton	CA	US	

US-CL-CURRENT: 345/519

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	RMC	Draw Desc	Image
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☐ 2. Document ID: US 20030112245 A1

L2: Entry 2 of 26

File: PGPB

Jun 19, 2003

PGPUB-DOCUMENT-NUMBER: 20030112245

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030112245 A1

TITLE: Single semiconductor graphics platform

PUBLICATION-DATE: June 19, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Dawallu, Kevin	Sunnyvale	CA	US	
Yang, Mingjian	Sunnyvale	CA	US	
Montrym, John	Los Altos	CA	US	
Kirk, David B.	San Francisco	CA	US	
Sabella, Paolo E.	Pleasanton	CA	US	
Papakipos, Matthew N.	Palo Alto	CA	US	
Voorhies, Douglas A.	Menlo Park	CA	US	
Foskett, Nicholas J.	Mountain View	CA	US	

US-CL-CURRENT: 345/506

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw Desc	Image
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☐ 3. Document ID: US 20030103054 A1

L2: Entry 3 of 26

File: PGPB

Jun 5, 2003

PGPUB-DOCUMENT-NUMBER: 20030103054

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030103054 A1

TITLE: Integrated graphics processing unit with antialiasing

PUBLICATION-DATE: June 5, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Montrym, John S.	Los Altos	CA	US	
Voorhies, Douglas A.	Menlo Park	CA	US	
Molnar, Steven E.	Chapel Hill	NC	US	

US-CL-CURRENT: 345/506

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw Desc	Image
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☐ 4. Document ID: US 20030103050 A1

L2: Entry 4 of 26

File: PGPB

Jun 5, 2003

PGPUB-DOCUMENT-NUMBER: 20030103050

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030103050 A1

TITLE: Masking system and method for a graphics processing framework embodied on a single semiconductor platform

PUBLICATION-DATE: June 5, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Dawallu, Kevin	Sunnyvale	CA	US	
Yang, Mingjian	Sunnyvale	CA	US	
Montrym, John	Los Altos	CA	US	
Kirk, David B.	San Francisco	CA	US	
Sabella, Paolo E.	Pleasanton	CA	US	
Papakipos, Matthew N.	Palo Alto	CA	US	
Voorhies, Douglas A.	Menlo Park	CA	US	
Foskett, Nicholas J.	Mountain View	CA	US	

US-CL-CURRENT: 345/426

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw Desc	Image
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☐ 5. Document ID: US 20030038808 A1

L2: Entry 5 of 26

File: PGPB

Feb 27, 2003

PGPUB-DOCUMENT-NUMBER: 20030038808
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030038808 A1

TITLE: Method, apparatus and article of manufacture for a sequencer in a
transform/lighting module capable of processing multiple independent execution threads

PUBLICATION-DATE: February 27, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Dawallu, Kevin	Sunnyvale	CA	US	
Montrym, John	Los Altos	CA	US	

US-CL-CURRENT: 345/506

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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MMC	Draw Desc	Image
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☐ 6. Document ID: US 20030034975 A1

L2: Entry 6 of 26

File: PGPB

Feb 20, 2003

PGPUB-DOCUMENT-NUMBER: 20030034975
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030034975 A1

TITLE: Lighting system and method for a graphics processor

PUBLICATION-DATE: February 20, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	

US-CL-CURRENT: 345/426; 345/536, 345/559, 345/561

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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MMC	Draw Desc	Image
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☐ 7. Document ID: US 20030020720 A1

L2: Entry 7 of 26

File: PGPB

Jan 30, 2003

PGPUB-DOCUMENT-NUMBER: 20030020720
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030020720 A1

TITLE: Method, apparatus and article of manufacture for a sequencer in a
transform/lighting module capable of processing multiple independent execution threads

PUBLICATION-DATE: January 30, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Dawallu, Kevin	Sunnyvale	CA	US	
Montrym, John	Los Altos	CA	US	

US-CL-CURRENT: 345/506

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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RMK	Draw Desc	Image
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☐ 8. Document ID: US 20020196259 A1

L2: Entry 8 of 26

File: PGPB

Dec 26, 2002

PGPUB-DOCUMENT-NUMBER: 20020196259

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020196259 A1

TITLE: Single semiconductor graphics platform with blending and fog capabilities

PUBLICATION-DATE: December 26, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Dawallu, Kevin	Sunnyvale	CA	US	
Yang, Mingjian	Sunnyvale	CA	US	
Montrym, John	Los Altos	CA	US	
Kirk, David B.	San Francisco	CA	US	
Sabella, Paolo E.	Pleasanton	CA	US	
Papakipos, Matthew N.	Palo Alto	CA	US	
Voorhies, Douglas A.	Menlo Park	CA	US	
Foskett, Nicholas J.	Mountain View	CA	US	

US-CL-CURRENT: 345/506

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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RMK	Draw Desc	Image
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☐ 9. Document ID: US 20020180740 A1

L2: Entry 9 of 26

File: PGPB

Dec 5, 2002

PGPUB-DOCUMENT-NUMBER: 20020180740

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020180740 A1

TITLE: Clipping system and method for a single graphics semiconductor platform

PUBLICATION-DATE: December 5, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Dawallu, Kevin	Sunnyvale	CA	US	
Yang, Mingjian	Sunnyvale	CA	US	
Montrym, John	Los Altos	CA	US	
Kirk, David B.	San Francisco	CA	US	
Sabella, Paolo E.	Pleasanton	CA	US	
Papakipos, Matthew N.	Palo Alto	CA	US	
Voorhies, Douglas A.	Menlo Park	CA	US	
Foskett, Nicholas J.	Mountain View	CA	US	

US-CL-CURRENT: 345/506

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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KWIC	Draw Desc	Image
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☐ 10. Document ID: US 20020105519 A1

L2: Entry 10 of 26

File: PGPB

Aug 8, 2002

PGPUB-DOCUMENT-NUMBER: 20020105519
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020105519 A1

TITLE: Clipping system and method for a graphics processing framework embodied on a single semiconductor platform

PUBLICATION-DATE: August 8, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Dawallu, Kevin	Sunnyvale	CA	US	
Yang, Mingjian	Sunnyvale	CA	US	
Montrym, John	Los Altos	CA	US	
Kirk, David B.	San Francisco	CA	US	
Sabella, Paolo E.	Pleasanton	CA	US	
Papakipos, Matthew N.	Palo Alto	CA	US	
Voorhies, Douglas A.	Menlo Park	CA	US	
Foskett, Nicholas J.	Mountain View	CA	US	

US-CL-CURRENT: 345/426; 345/506, 345/519

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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KWIC	Draw Desc	Image
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Terms	Documents
-------	-----------

L1 and logic unit

26

Display Format:

-

Change Format

Previous Page

Next Page

WEST[Generate Collection](#)[Print](#)**Search Results - Record(s) 11 through 20 of 26 returned.**☐ 11. Document ID: US 20020047846 A1

L2: Entry 11 of 26

File: PGPB

Apr 25, 2002

PGPUB-DOCUMENT-NUMBER: 20020047846
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020047846 A1

TITLE: System, method and computer program product for performing a scissor operation in a graphics processing framework embodied on a single semiconductor platform

PUBLICATION-DATE: April 25, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Dawallu, Kevin	Sunnyvale	CA	US	
Yang, Mingjian	Sunnyvale	CA	US	
Montrym, John	Los Altos	CA	US	
Kirk, David B.	San Francisco	CA	US	
Sabella, Paolo E.	Pleasanton	CA	US	
Papakipos, Matthew N.	Palo Alto	CA	US	
Voorhies, Douglas A.	Menlo Park	CA	US	
Foskett, Nicholas J.	Mountain View	CA	US	

US-CL-CURRENT: [345/522](#); [345/426](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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PMC	Draw Deso	Image
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☐ 12. Document ID: US 20020027553 A1

L2: Entry 12 of 26

File: PGPB

Mar 7, 2002

PGPUB-DOCUMENT-NUMBER: 20020027553
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020027553 A1

TITLE: Diffuse-coloring system and method for a graphics processing framework embodied on a single semiconductor platform

PUBLICATION-DATE: March 7, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Dawallu, Kevin	Sunnyvale	CA	US	
Yang, Mingjian	Sunnyvale	CA	US	
Montrym, John	Los Altos	CA	US	
Kirk, David B.	San Francisco	CA	US	
Sabella, Paolo E.	Pleasanton	CA	US	
Papakipos, Matthew N.	Palo Alto	CA	US	
Voorhies, Douglas A.	Menlo Park	CA	US	
Foskett, Nicholas J.	Mountain View	CA	US	

US-CL-CURRENT: [345/426](#); [345/519](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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KWIC	Draw Desc	Image
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☐ 13. Document ID: US 20010017626 A1

L2: Entry 13 of 26

File: PGPB

Aug 30, 2001

PGPUB-DOCUMENT-NUMBER: 20010017626

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20010017626 A1

TITLE: Graphics processing unit with transform module capable of handling scalars and vectors

PUBLICATION-DATE: August 30, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Kirk, David B.	San Francisco	CA	US	
Sabella, Paolo E.	Pleasanton	CA	US	

US-CL-CURRENT: [345/501](#); [345/561](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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KWIC	Draw Desc	Image
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☐ 14. Document ID: US 20010005209 A1

L2: Entry 14 of 26

File: PGPB

Jun 28, 2001

PGPUB-DOCUMENT-NUMBER: 20010005209

PGPUB-FILING-TYPE: new-utility

DOCUMENT-IDENTIFIER: US 20010005209 A1

TITLE: Method, apparatus and article of manufacture for a transform module in a graphics processor

PUBLICATION-DATE: June 28, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lindholm, John Erik	Cupertino	CA	US	
Moy, Simon	Mountain View	CA	US	
Kirk, David B.	San Francisco	CA	US	
Sabella, Paolo E.	Pleasanton	CA	US	

US-CL-CURRENT: 345/506; 345/426, 345/501

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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MMC	Draw Desc	Image
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☐ 15. Document ID: US 6577309 B2

L2: Entry 15 of 26

File: USPT

Jun 10, 2003

US-PAT-NO: 6577309

DOCUMENT-IDENTIFIER: US 6577309 B2

TITLE: System and method for a graphics processing framework embodied utilizing a single semiconductor platform

DATE-ISSUED: June 10, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lindholm; John Erik	Cupertino	CA		
Moy; Simon	Mountain View	CA		
Dawallu; Kevin	Sunnyvale	CA		
Yang; Mingjian	Sunnyvale	CA		
Montrym; John	Los Altos	CA		
Kirk; David B.	San Francisco	CA		
Sabella; Paolo E.	Pleasanton	CA		
Papakipos; Matthew N.	Palo Alto	CA		
Voorhies; Douglas A.	Menlo Park	CA		
Foskett; Nicholas J.	Mountain View	CA		

US-CL-CURRENT: 345/426; 345/418

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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MMC	Draw Desc	Image
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☐ 16. Document ID: US 6573900 B1

L2: Entry 16 of 26

File: USPT

Jun 3, 2003

US-PAT-NO: 6573900

DOCUMENT-IDENTIFIER: US 6573900 B1

TITLE: Method, apparatus and article of manufacture for a sequencer in a transform/lighting module capable of processing multiple independent execution threads

DATE-ISSUED: June 3, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lindholm; John Erik	Cupertino	CA		
Moy; Simon	Mountain View	CA		
Dawallu; Kevin	Sunnyvale	CA		
Montrym; John	Los Altos	CA		

US-CL-CURRENT: 345/537; 345/531, 345/533, 345/556

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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MM	Draw Desc	Image
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☐ 17. Document ID: US 6515671 B1

L2: Entry 17 of 26

File: USPT

Feb 4, 2003

US-PAT-NO: 6515671

DOCUMENT-IDENTIFIER: US 6515671 B1

TITLE: Method, apparatus and article of manufacture for a vertex attribute buffer in a graphics processor

DATE-ISSUED: February 4, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lindholm; John Erik	Cupertino	CA		
Moy; Simon	Mountain View	CA		

US-CL-CURRENT: 345/506; 345/522

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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MM	Draw Desc	Image
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☐ 18. Document ID: US 6504542 B1

L2: Entry 18 of 26

File: USPT

Jan 7, 2003

US-PAT-NO: 6504542

DOCUMENT-IDENTIFIER: US 6504542 B1

TITLE: Method, apparatus and article of manufacture for area rasterization using sense points

DATE-ISSUED: January 7, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Voorhies; Douglas A.	Menlo Park	CA		
Foskett; Nicholas J.	Mountain View	CA		

US-CL-CURRENT: 345/441

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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MM	Draw Desc	Image
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☐ 19. Document ID: US 6462737 B2

L2: Entry 19 of 26

File: USPT

Oct 8, 2002

US-PAT-NO: 6462737
DOCUMENT-IDENTIFIER: US 6462737 B2

TITLE: Clipping system and method for a graphics processing framework embodied on a single semiconductor platform

DATE-ISSUED: October 8, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lindholm; John Erik	Cupertino	CA		
Moy; Simon	Mountain View	CA		
Dawallu; Kevin	Sunnyvale	CA		
Yang; Mingjian	Sunnyvale	CA		
Montrym; John	Los Altos	CA		
Kirk; David B.	San Francisco	CA		
Sabella; Paolo E.	Pleasanton	CA		
Papakipos; Matthew N.	Palo Alto	CA		
Voorhies; Douglas A.	Menlo Park	CA		
Foskett; Nicholas J.	Mountain View	CA		

US-CL-CURRENT: [345/426](#); [345/418](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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KVMC	Draw Desc	Image
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☐ 20. Document ID: US 6452595 B1

L2: Entry 20 of 26

File: USPT

Sep 17, 2002

US-PAT-NO: 6452595
DOCUMENT-IDENTIFIER: US 6452595 B1

TITLE: Integrated graphics processing unit with antialiasing

DATE-ISSUED: September 17, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Montrym; John S.	Los Altos	CA		
Voorhies; Douglas A.	Menlo Park	CA		
Molnar; Steven E.	Chapel Hill	NC		

US-CL-CURRENT: [345/426](#); [345/519](#), [345/611](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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KVMC	Draw Desc	Image
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Generate Collection

Print

Terms

Documents

L1 and logic unit

26

Display Format:

[Previous Page](#) [Next Page](#)

WEST[Generate Collection](#)[Print](#)**Search Results - Record(s) 21 through 26 of 26 returned.**☐ **21. Document ID: US 6417851 B1**

L2: Entry 21 of 26

File: USPT

Jul 9, 2002

US-PAT-NO: 6417851

DOCUMENT-IDENTIFIER: US 6417851 B1

**** See image for Certificate of Correction ****

TITLE: Method and apparatus for lighting module in a graphics processor

DATE-ISSUED: July 9, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lindholm; John Erik	Cupertino	CA		
Moy; Simon	Mountain View	CA		

US-CL-CURRENT: [345/426](#); [345/501](#), [345/506](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw Desc	Image
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☐ **22. Document ID: US 6353439 B1**

L2: Entry 22 of 26

File: USPT

Mar 5, 2002

US-PAT-NO: 6353439

DOCUMENT-IDENTIFIER: US 6353439 B1

**** See image for Certificate of Correction ****

TITLE: System, method and computer program product for a blending operation in a transform module of a computer graphics pipeline

DATE-ISSUED: March 5, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lindholm; John Erik	Cupertino	CA		
Moy; Simon	Mountain View	CA		
Kirk; David B.	San Francisco	CA		
Sabella; Pao	Pleasanton	CA		

US-CL-CURRENT: [345/561](#); [345/426](#), [345/427](#), [345/506](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw Desc	Image
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☐ **23. Document ID: US 6342888 B1**

L2: Entry 23 of 26

File: USPT

Jan 29, 2002

US-PAT-NO: 6342888
DOCUMENT-IDENTIFIER: US 6342888 B1
** See image for Certificate of Correction **

TITLE: Graphics processing unit with an integrated fog and blending operation

DATE-ISSUED: January 29, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lindholm; John Erik	Cupertino	CA		
Moy; Simon	Mountain View	CA		
Dawallu; Kevin	Sunnyvale	CA		
Yang; Mingjian	Sunnyvale	CA		
Montrym; John	Los Altos	CA		
Kirk; David B.	San Francisco	CA		
Sabella; Paolo E.	Pleasanton	CA		
Papakipos; Matthew N.	Palo Alto	CA		
Voorhies; Douglas A.	Menlo Park	CA		
Foskett; Nicholas J.	Mountain View	CA		

US-CL-CURRENT: 345/426; 345/418

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw Desc	Image
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☐ 24. Document ID: US 6198488 B1

L2: Entry 24 of 26

File: USPT

Mar 6, 2001

US-PAT-NO: 6198488
DOCUMENT-IDENTIFIER: US 6198488 B1
** See image for Certificate of Correction **

TITLE: Transform, lighting and rasterization system embodied on a single semiconductor platform

DATE-ISSUED: March 6, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lindholm; John Erik	Cupertino	CA		
Moy; Simon	Mountain View	CA		
Dawallu; Kevin	Sunnyvale	CA		
Yang; Mingjian	Sunnyvale	CA		
Montrym; John	Los Altos	CA		
Kirk; David B.	San Francisco	CA		
Sabella; Paolo E.	Pleasanton	CA		
Papakipos; Matthew N.	Palo Alto	CA		
Voorhies; Douglas A.	Menlo Park	CA		
Foskett; Nicholas J.	Mountain View	CA		

US-CL-CURRENT: 345/426; 345/418, 345/419

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC	Draw Desc	Image
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☐ 25. Document ID: US 20030034975 A1

L2: Entry 25 of 26

File: DWPI

Feb 20, 2003

DERWENT-ACC-NO: 2003-417766

DERWENT-WEEK: 200339

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TITLE: Lighting system for graphics processor, has memory storing constants and variables used when processing vertex data

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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☐ 26. Document ID: US 6417851 B1

L2: Entry 26 of 26

File: DWPI

Jul 9, 2002

DERWENT-ACC-NO: 2002-680826

DERWENT-WEEK: 200339

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TITLE: Lighting system for graphic processor has memory coupled to either input of multiplication logic unit or output of arithmetic logic unit to store constants and variables used when processing vertex data

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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Patent Assignment Abstract of Title

Total Assignments: 2

Application #: 09454524 **Filing Dt:** 12/06/1999 **Patent #:** 6417851 **Issue Dt:** 07/09/2002
PCT #: NONE **Publication #:** NONE **Pub Dt:**
Inventors: JOHN ERIK LINDHOLM, SIMON MOY
Title: METHOD AND APPARATUS FOR LIGHTING MODULE IN A GRAPHICS PROCESSOR

Assignment: 1

Reel/Frame: 010456/0751 **Received:** 12/27/1999 **Recorded:** 12/06/1999 **Mailed:** 03/20/2000 **Pages:** 2

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignors: LINDHOLM, JOHN ERIK **Exec Dt:** 12/01/1999
MOY, SIMON **Exec Dt:** 12/02/1999

Assignee: NVIDIA
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Correspondent: HICKMAN STEPHENS ET AL
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Assignment: 2

Reel/Frame: 012115/0982 **Received:** 08/31/2001 **Recorded:** 08/28/2001 **Mailed:** 11/01/2001 **Pages:** 3

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignors: LINDHOLM, JOHN ERIK **Exec Dt:** 12/01/1999
MOY, SIMON **Exec Dt:** 12/02/1999

Assignee: NVIDIA CORPORATION
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